

FIG. 3

4/8 \sim 3(3) \sim 3(2) \sim 3(2) $\sim 3(3)$ $\sim 3(1)$ **FADER FADER FADER FADER FADER FADER** CH3 CH2 CH₁ CH3 CH2 CH) 22 INPUT PATCH INPUT PATCH FIG. 4B FIG. 4A 22 INPUT PORT INPUT PORT INPUT PORT INPUT PORT INPUT PORT INPUT PORT 23(3)-23(2)-23(3) 23(2) 23(1) 23(1) RECORDER RECORDER RECORDER RECORDER RECORDER RECORDER 12(2)~ 12(2)~ 12(3)~ 12(3)~

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FIG. 5A

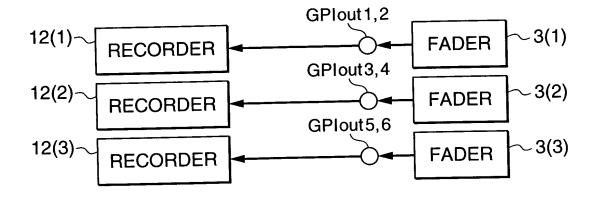


FIG. 5B

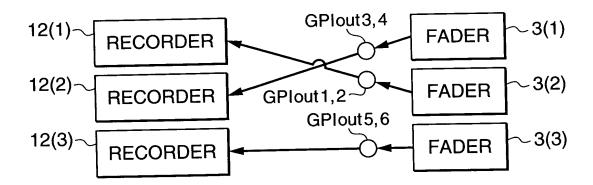
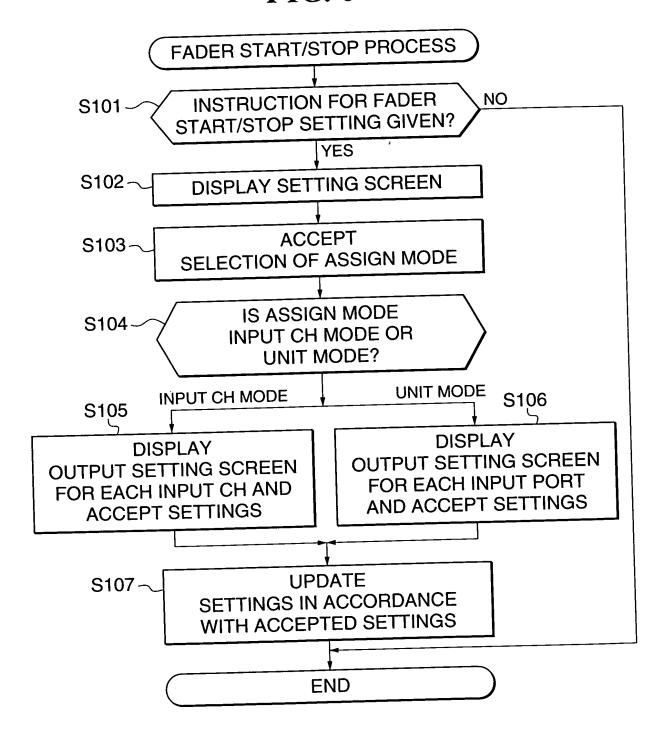


FIG. 6



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FIG. 7A

32 33 34 INPUT CH **ASSIGN MODE** OUTPUT **OUTPUT** PARAMETER **FORM DEVICE** INPUT CH START: OUTPUT PORT 1 CH1 **GPI ENGINE** STOP: OUTPUT PORT 2 UNIT 1 CH3 **MMC ENGINE** UNIT 2

FIG. 7B

32 33 35 **OUTPUT ASSIGN MODE PARAMETER INPUT OUTPUT FORM DEVICE PORT INPUT CH INPUT** START: OUTPUT PORT 1 **GPI ENGINE** PORT (1) STOP: OUTPUT PORT 2 UNIT 1 **INPUT MMC ENGINE** UNIT 2 PORT (2) **INPUT** PORT (3)

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FIG. 8

